

375	371	Subclass	ISSUE CLASSIFICATION
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PATENT NUMBER

U.S. UTILITY Patent Application

ICL
50640

2 M.H.	O.I.P.E.	PATENT DATE
SCANNED	Q.A.	4/1

APPLICATION NO. 09/584728	CONT/PRIOR F	CLASS 327 375	SUBCLASS 371	ART UNIT 2876 2631	EXAMINER TRAN, KHAN CORRELLUS C
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TITLE APPLICANTS

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Digital synchronous circuit for stably generating output clock
synchronized with input data

PTO-2040
12/99

ISSUING CLASSIFICATION							
ORIGINAL		CROSS REFERENCE(S)					
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
375	371	375	375				
INTERNATIONAL CLASSIFICATION		327	163				
H04L	07/033	326	96				

☐ Continued on Issue Slip inside File Jacket

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg. 6	Figs. Drwg. 8	Print Fig. 1	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____					
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Primary Examiner) (Date)			ISSUE FEE	
				Amount Due	Date Paid
	_____ (Legal Instruments Examiner) (Date)			ISSUE BATCH NUMBER	

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